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|------------------|------------|------------|---|----------------|------------------------|-----------------|-----------------------------|------|-------|----------|-----|------|
| US 2005025801 A1 | 20051114 | 144 | System and method for frequency & CMOS differential pair | 200/254 | | | Kremenezek, Michael | P | C | E | D | R |
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| US 2005025804 A1 | 20051127 | 38 | adaptive radio transceiver with offset PLL with bypassing modes | 455/260 | 455/260 | | Reichstein, Ahmadreza et al | P | C | E | D | R |
| US 2005025805 A1 | 20051127 | 39 | adaptive radio transceiver with frequency synthesis capacitor | 455/157.2 | 455/157.2 | | Reichstein, Ahmadreza et al | P | R | F | M | R |
| US 2005025806 A1 | 20051127 | 69 | adaptive radio transceiver with noise suppression | 455/271 | | | Reichstein, Ahmadreza et al | P | R | M | M | R |
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| US 2005025809 A1 | 20051127 | 96 | System and method for on-chip filter tuning | 455/240 | 257/227.046 455/312 | | Wachowiak, Peter M. S. | P | C | E | D | R |
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